

[illegible]

Fig. 2

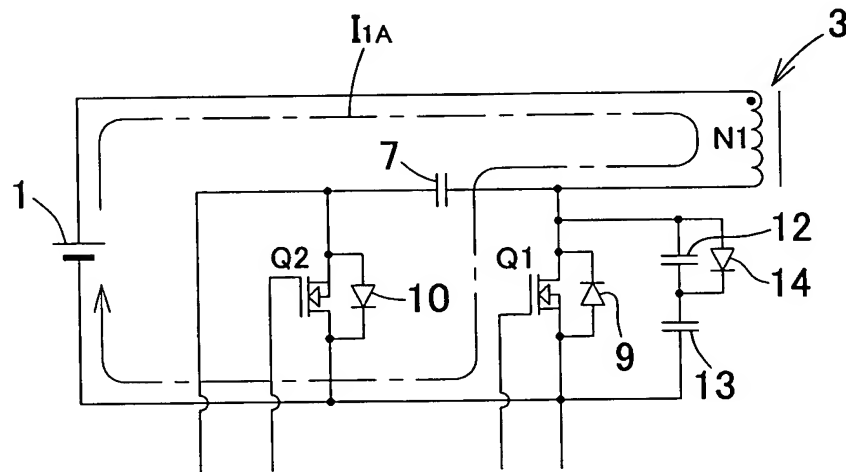


Fig. 3

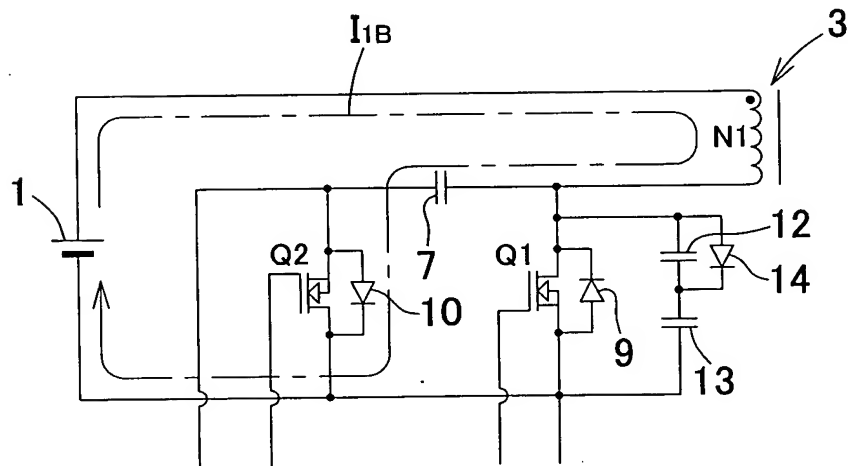
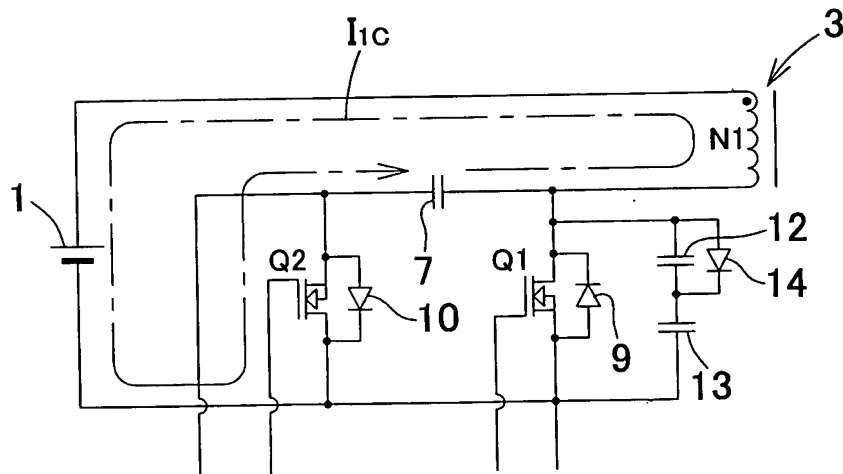


Fig. 4



The diagram shows a power MOSFET driver circuit. A DC source 1 is connected to the gates of MOSFETs Q1 and Q2. Q2 is a bootstrap MOSFET that charges a bootstrap capacitor 10. Q1 is the main power MOSFET. Its drain is connected to a transformer primary N1. The secondary of the transformer is connected to a load 12. A diode 14 and a capacitor 13 are connected in parallel across the load. The gate of Q1 is driven by a pulse-width modulated signal V_{G1} from a driver 5. The gate of Q2 is driven by a pulse-width modulated signal V_{G2} from a driver 6. The drain of Q1 is also connected to a feedback network consisting of a resistor R1, a resistor R2, and a comparator 25. The comparator 25 is also connected to a reference voltage V_{cc} and a feedback signal from the load 12. The output of the comparator 25 is connected to a driver 24, which drives the gate of Q3. Q3 is a MOSFET that drives a load 23 through a diode 26. The drain of Q3 is connected to a load 24 and a feedback signal from the load 23. The drain of Q3 is also connected to a feedback signal from the load 23. The drain of Q3 is connected to a load 24 and a feedback signal from the load 23. The drain of Q3 is connected to a load 24 and a feedback signal from the load 23.

Fig. 6

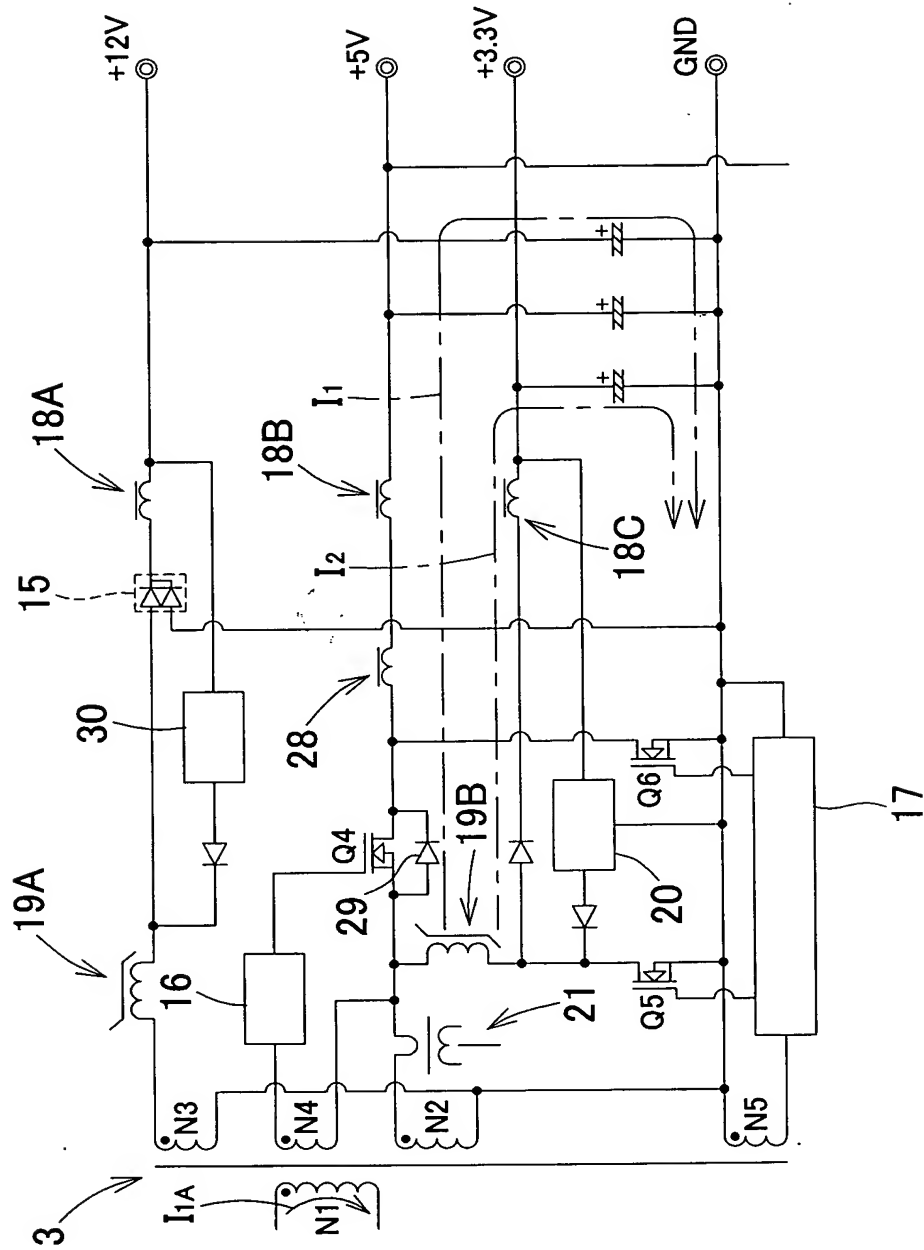


Fig. 7

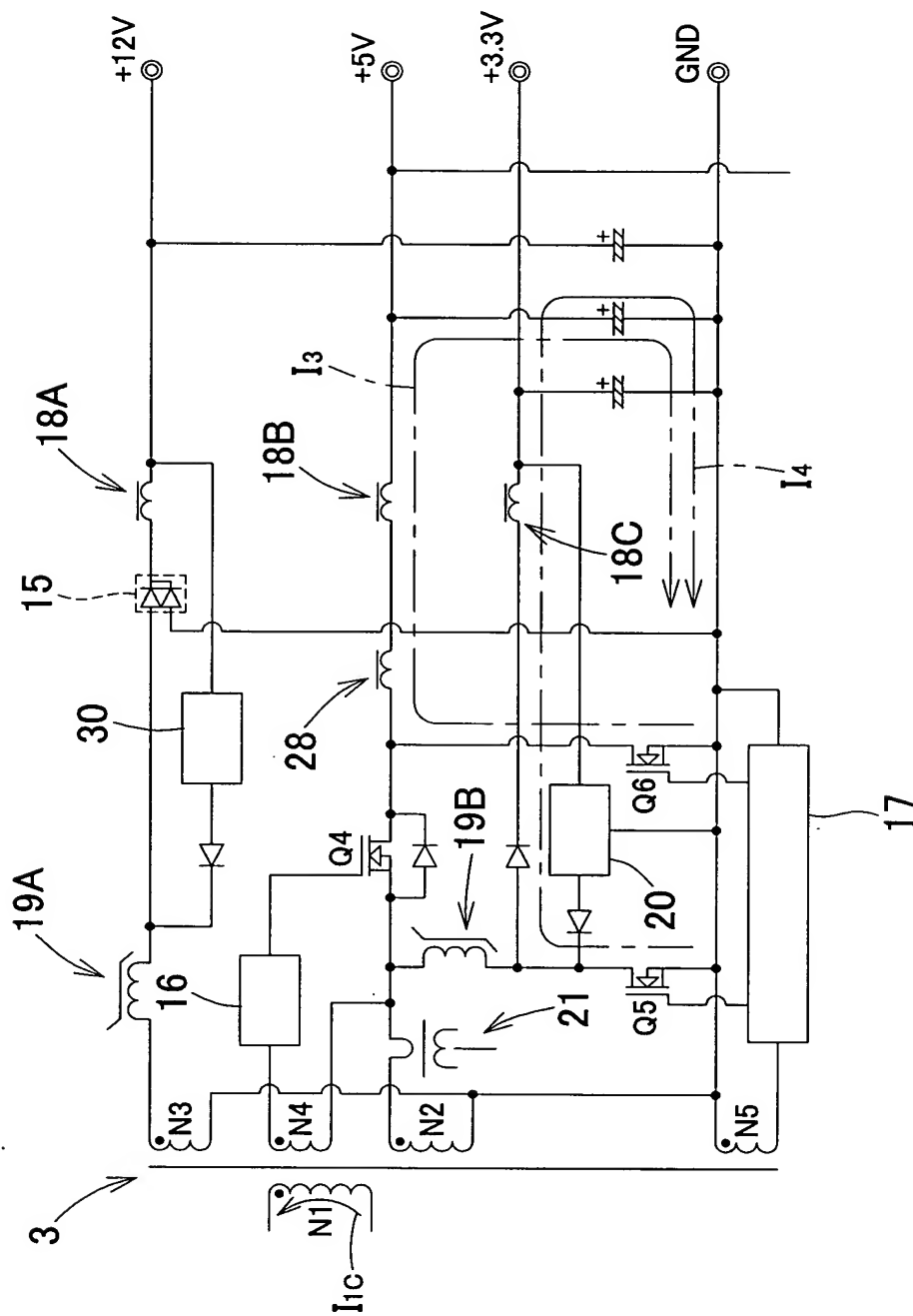


Fig. 8

